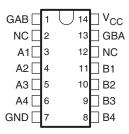


### **QUADRUPLE BUS TRANSCEIVERS**

### **FEATURES**

- Two-Way Asynchronous Communication Between Data Buses
- PNP Inputs Reduce D-C Loading
- Hysteresis (Typically 400 mV) at Inputs Improves Noise Margin

SN54LS243 . . . J OR W PACKAGE SN74LS243 . . . D, N, OR NS PACKAGE (TOP VIEW)



# FUNCTION TABLE (EACH TRANSCEIVER)

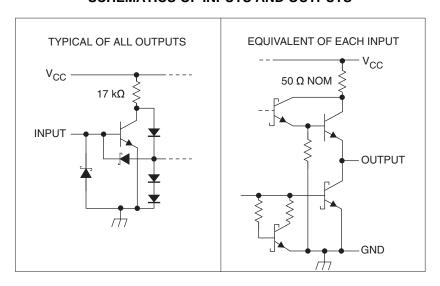
INP	UTS	Chlynd C242
GAB	GBA	SNxxLS243
L	L	A to B
Н	Н	B to A
Н	L	Isolation
L	Н	Latch A and B (A = B)

### **DESCRIPTION**

These four-data-line transceivers are designed for asynchronous two-way communications between data buses. SN74LS243 can be used to drive terminated lines down to  $133~\Omega$ .

SN54LS243 is characterized for operation over the full military temperature range of -55°C to 125°C. SN74LS243 is characterized for operation from 0°C to 70°C.

#### **SCHEMATICS OF INPUTS AND OUTPUTS**

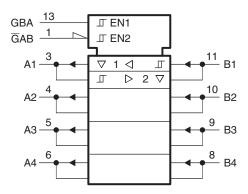




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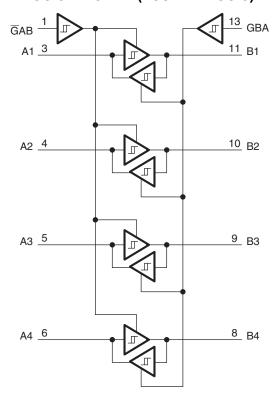


### **LOGIC SYMBOL**



A. These symbols are in accordance with ANSI/EEE Std. 91-1984 and IEC Publication 617-12.

### **LOGIC DIAGRAM (POSITIVE LOGIC)**





### **ABSOLUTE MAXIMUM RATINGS**(1)

			MIN	MAX	UNIT
V <sub>CC</sub>	Supply voltage <sup>(2)</sup>			7	V
V <sub>IN</sub>	Input voltage		7	V	
	OFF-state output voltage			5.5	V
_		SN54LS243	-55	125	00
T <sub>A</sub>	Operating free-air temperature range	SN74LS243	0	70	°C
T <sub>stg</sub>	Storage temperature range		-65	150	°C

<sup>(1)</sup> Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

### RECOMMENDED OPERATING CONDITIONS

over operating free-air temperature range (unless otherwise noted)

		SNS	SN54LS243				SN74LS243			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT		
$V_{CC}$	Supply voltage <sup>(1)</sup>	4.5	5	5.5	4.75	5	5.25	V		
V <sub>IH</sub>	High-level input voltage	2			2			V		
V <sub>IL</sub>	Low-level input voltage			0.7			0.8	V		
I <sub>OH</sub>	High-level output voltage			-12			-15	mA		
I <sub>OL</sub>	Low-level output voltage			12			24	mA		
T <sub>A</sub>	Operating free-air temperature	-55		125	0		70	°C		

<sup>(1)</sup> Voltage values are with respect to network ground terminal.

<sup>(2)</sup> Voltage values are with respect to network ground terminal.



### **ELECTRICAL CHARACTERISTICS**

over operating free-air temperature range (unless otherwise noted)

DADAMETER		_	TEST CONDITIONS <sup>(1)</sup>			4LS243		SN	N74LS243	3	
	PARAMETER TEST CONDITIONS <sup>(1)</sup>		ONS(")	MIN TYP <sup>(2)</sup>		MAX	MIN	TYP <sup>(2)</sup>	MAX	UNIT	
V <sub>IK</sub>	A or B	V <sub>CC</sub> = MIN,	$I_1 = -18 \text{ mA}$				-1.5			-1.5	V
Hyster	esis (V <sub>T+</sub> – V <sub>T-</sub> )	$V_{CC} = MIN,$			0.2	0.4		0.2	0.4		٧
V		V <sub>CC</sub> = MIN,	V - 2 V	$V_{IL} = MAX,$ $I_{OH} = -3 \text{ mA}$	2.4	3.1		2.4	3.1		٧
V <sub>OH</sub>		V <sub>CC</sub> = IVIIIN,	V <sub>IH</sub> = 2 V,	$V_{IL} = 0.5 \text{ V},$ $I_{OH} = \text{MAX}$	2			2			
\/		V <sub>CC</sub> = MIN,	$V_{IH} = 2 V$ ,	I <sub>OL</sub> = 12 mA		0.25	0.4		0.25	0.4	>
$V_{OL}$		$V_{IL} = MAX$		I <sub>OL</sub> = 24 mA					0.35	0.5	V
l <sub>ozh</sub>		$V_{CC} = MIN,$ $V_{IL} = MAX,$	V <sub>IH</sub> = 2 V,	V <sub>O</sub> = 2.7 V			40			40	μΑ
l <sub>OZL</sub>		$V_{CC} = MIN,$ $V_{IL} = MAX,$	V <sub>IH</sub> = 2 V,	V <sub>O</sub> = 0.4 V			-200			-200	μΑ
	A or B	\/		V <sub>I</sub> = 5.5 V			0.1			0.1	A
I <sub>I</sub>	GAB or GBA	$V_{CC} = MAX,$		V <sub>I</sub> = 7 V			0.1			0.1	mA
I <sub>IH</sub>		$V_{CC} = MAX,$					20			20	μΑ
	A inputs	V <sub>CC</sub> = MAX, GAB and GB	· ·				-0.2			-0.2	
I <sub>IL</sub>	B inputs	V <sub>CC</sub> = MAX, GAB and GB	· ·				-0.2			-0.2	mA
	GAB or GBA	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 0.4 V,				-0.2			-0.2	Ì
I <sub>OS</sub>		$V_{CC} = MAX$			-40		-225	-40		-225	mA
	Outputs high					22	38		22	38	
I <sub>CC</sub>	Outputs low	$V_{CC} = MAX$	V <sub>CC</sub> = MAX, Outputs open,			29	50		29	50	mA
•00	All outputs disabled	Outputs open				32	54		32	54	1117

<sup>(1)</sup> For conditions shown as MIN or MAX, use the appropriate value specified under "recommended operating conditions."

### **SWITCHING CHARACTERISTICS**

 $V_{CC} = 5 \text{ V}, T_A = 25^{\circ}\text{C}$ 

DADAMETED	PARAMETER TEST CONDITIONS		SN5		SN7	UNIT			
PARAMETER			MIN	TYP	MAX	MIN	TYP	MAX	UNII
t <sub>PLH</sub>				9	14		12	18	ns
t <sub>PHL</sub>	$R_1 = 667 \Omega$	$C_1 = 45 \text{ pF}$		12	18		12	18	ns
t <sub>PZL</sub>	$R_L = 007 \Omega$	OL = 45 pi		20	30		20	30	ns
t <sub>PZH</sub>				15	23		15	23	ns
t <sub>PLZ</sub>	$R_1 = 667 \Omega$	$C_1 = 5 pF$		10	20		10	20	ns
t <sub>PHZ</sub>	$N_L = 007 \Omega$	GL = 5 pr		15	25		15	25	ns

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 <sup>(2)</sup> All typical values are at V<sub>CC</sub> = 5 V, T<sub>A</sub> = 25°C.
 (3) I<sub>CC</sub> is measured with transceivers eabled in one direction only, or with all transceivers disabled.

#### PACKAGE OPTION ADDENDUM



i.com 18-Sep-2008

#### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
80020022A	OBSOLETE			20		TBD	Call TI	Call TI
8002002CA	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
8002002DA	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type
SN54LS243J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SN74LS243D	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS243DE4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS243DG4	ACTIVE	SOIC	D	14	50	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS243DR	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS243DRE4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS243DRG4	ACTIVE	SOIC	D	14	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
SN74LS243J	OBSOLETE	CDIP	J	14		TBD	Call TI	Call TI
SN74LS243N	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SN74LS243N3	OBSOLETE	PDIP	N	14		TBD	Call TI	Call TI
SN74LS243NE4	ACTIVE	PDIP	N	14	25	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type
SNJ54LS243FK	OBSOLETE			20		TBD	Call TI	Call TI
SNJ54LS243J	ACTIVE	CDIP	J	14	1	TBD	A42 SNPB	N / A for Pkg Type
SNJ54LS243W	ACTIVE	CFP	W	14	1	TBD	A42	N / A for Pkg Type

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <a href="http://www.ti.com/productcontent">http://www.ti.com/productcontent</a> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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### **PACKAGE OPTION ADDENDUM**

18-Sep-2008

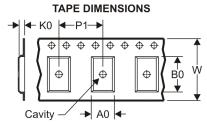
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### TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
В0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

Device	_	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS243DR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1





#### \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS243DR	SOIC	D	14	2500	346.0	346.0	33.0

### 14 LEADS SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package is hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
- E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

## W (R-GDFP-F14)

### CERAMIC DUAL FLATPACK



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14 and JEDEC MO-092AB



## D (R-PDSO-G14)

### PLASTIC SMALL-OUTLINE PACKAGE



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AB.



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